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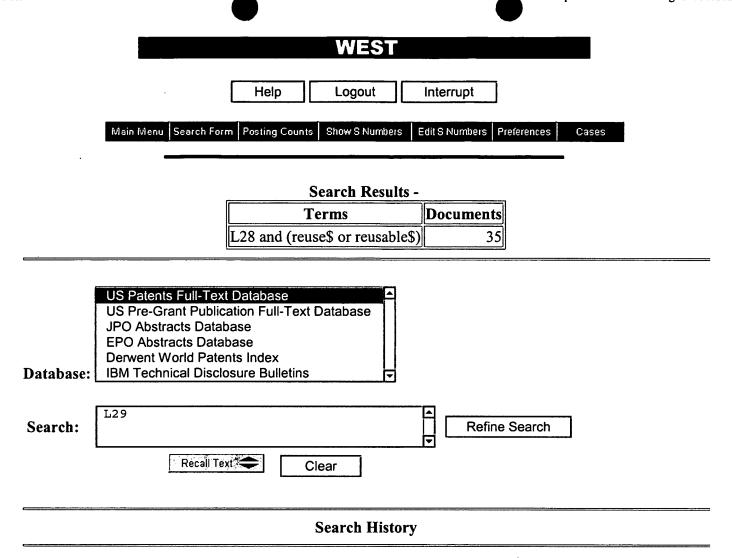
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<u>L22</u>	reus\$ near8 (instruction\$ or compute or computation\$) and vector\$ and symbol\$ and (sequence near5 execution\$)	26	<u>L22</u>
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<u>L19</u>	value\$ and register\$ and input near5 operand and (reuse\$ or resuable\$)	258	<u>L19</u>
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<u>L17</u>	19 and (repeat\$ or recurrent\$ or redun\$)	0	<u>L17</u>
<u>L16</u>	19 and (ascend\$ or greater\$)	0	<u>L16</u>
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<u>L10</u>	L9 and resua\$	0	<u>L10</u>
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<u>L6</u>	L5 and integer\$	54	<u>L6</u>
<u>L5</u>	L4 and (multi\$ or plural\$) near5 dimension\$	54	<u>L5</u>
<u>L4</u>	L3 and vector\$	80	<u>L4</u>
<u>L3</u>	11 and (phras\$ or text\$)	103	<u>L3</u>
<u>L2</u>	L1 and (live-in or live in)	0	<u>L2</u>
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Emery D. Berger, Benjamin G. Zorn, Kathryn S. McKinley ACM SIGPLAN Notices, Proceedings of the 17th ACM conference on Object-oriented programming, systems, languages, and applications November 2002

Volume 37 Issue 11

Programmers hoping to achieve performance improvements often use custom memory allocators. This in-depth study examines eight applications that use custom allocators. Surprisingly, for six of these applications, a state-of-the-art general-purpose allocator (the Lea allocator) performs as well as or better than the custom allocators. The two exceptions use regions, which deliver higher performance (improvements of up to 44%). Regions also reduce programmer burden and eliminate a source of memory ...

Better exploration of region-level value locality with integrated

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Youfeng Wu, Dong-Yuan Chen, Jesse Fang

ACM SIGARCH Computer Architecture News, Proceedings of the 28th annual international symposium on on Computer architecture May 2001 Volume 29 Issue 2



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